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# A typed parallel $\lambda$ -calculus via 1-depth intermediate proofs<sup>\*</sup>

Federico Aschieri<sup>1</sup>, Agata Ciabattoni<sup>1</sup>, and Francesco A. Genco<sup>2</sup>

 <sup>1</sup> TU Wien, Vienna, Austria federico.aschieri@tuwien.ac.at, agata@logic.at
 <sup>2</sup> IHPST, Université Paris 1 and CNRS, Paris, France frgenco@gmail.com

#### Abstract

We introduce a Curry–Howard correspondence for a large class of intermediate logics characterized by intuitionistic proofs with non-nested applications of rules for classical disjunctive tautologies (1-depth intermediate proofs). The resulting calculus, we call it  $\lambda_{\parallel}$ , is a strongly normalizing parallel extension of the simply typed  $\lambda$ -calculus. Although simple, the  $\lambda_{\parallel}$  reduction rules can model arbitrary process network topologies, and encode interesting parallel programs ranging from numeric computation to algorithms on graphs.

## 1 Introduction

The fundamental connection between logic and computation, known as Curry–Howard correspondence, relates logics and computational systems. Originally introduced for intuitionistic logic **IL** and simply typed  $\lambda$ -calculus, it has been extended to many different logics (classical [26], linear [9, 5], modal [25]...) and notions of computation, see, e.g., [30] for an overview.

A recent addition to them is the discovery in [4] of the connection between propositional logics intermediate between classical logic and **IL**, and concurrent extensions of the simply typed  $\lambda$ -calculus. More precisely, the considered logics extend **IL** with the classical disjunctive tautologies interpreted as synchronization schemata in [14], i.e. axiom schemata of the form  $(F_1 \rightarrow G_1) \vee \ldots \vee (F_m \rightarrow G_m)$ . This general result was preceeded by Curry–Howard correspondences for the *particular cases* of Gödel-Dummett logic (**IL** with LIN =  $(A \rightarrow B) \vee (B \rightarrow A)$ ) and classical logic (**IL** with EM =  $A \vee \neg A$ ) in [2, 3]. All these logics possess cut-free hypersequent calculi – a generalization of sequent calculi consisting of parallel compositions of sequents that can "communicate"; our results confirmed Avron's conjecture [7] of the connection between (intermediate logics characterized by cut-free) hypersequent calculi and concurrency. Although the resulting typed concurrent  $\lambda$ -calculi provided an adequate computational interpretation of these logics, they are quite sophisticated and not easy to use as programming languages. Their main reductions are: *intuitionistic reductions* – which are the usual computational rules for the simply typed  $\lambda$ -calculus [16], *permutation reductions* – needed to prove weak normalization, and the reductions implementing communications among simply typed  $\lambda$ -terms; these are divided into

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basic cross reductions – simple reductions for the natural deduction version of the characteristic hypersequent rules, and *full cross reductions* – needed for the subformula property – which enable the transmission of messages that depend on their computational environment by using code mobility concepts [15] such as that of closure [20].

The aim of this work is to present a version of the calculi in [4, 2, 3] suitable for programming. We show that a simplified version of these calculi is expressive enough to model arbitrary process network topologies and to encode interesting parallel algorithms taken from [22]. Inspired by [12, 13] we base our Curry–Howard correspondence on 1-depth<sup>1</sup> intermediate proofs, i.e., **IL** proofs with *non-nested* applications of the rules for classical disjunctive tautologies. This leads to  $\lambda_{\parallel}$ , an easy-to-use and yet expressive parallel extension of the simply typed  $\lambda$ -calculus. Consisting only of *intuitionistic reductions* and (a simplified version of) *basic cross reductions*, the reduction rules of  $\lambda_{\parallel}$  always terminate, regardless of the reduction strategy.

Motivated by the tool Grace<sup>2</sup> [19] which allows programmers in the parallel functional language Eden to specify a network of processes as a directed graph and provides special constructs to generate the actual network topology, we also provide an automatic procedure to extract  $\lambda_{\parallel}$  typing rules from any communication topology in such a way that the typed terms can only communicate according to the topology. The idea of enforcing network topologies with types is also present in [10] in the different context of the  $\pi$ -calculus [24] – the most widespread formalism for *modeling* concurrent systems.

We consider classical disjunctive tautologies in the (intuitionistically equivalent) form

$$(\mathbb{A}_1 \to \mathbb{A}_1 \land \mathbb{B}_1) \lor \ldots \lor (\mathbb{A}_m \to \mathbb{A}_m \land \mathbb{B}_m)$$
(1)

such that all  $\mathbb{B}_i$  are either  $\perp$  or a conjunction of some  $\mathbb{A}_1, \ldots, \mathbb{A}_m$ . These axioms can indeed encode any reflexive directed graphs as follows:  $\mathbb{A}_i$  represents a process in the network specified by the graph, and  $\mathbb{B}_i$  is the list of processes that are connected to  $\mathbb{A}_i$  by an outgoing arc, or  $\perp$  if there are no such processes. The intuitive reading of  $(\mathbb{A}_i \to \mathbb{A}_i \land \mathbb{B}_i)$  is that all conjuncts in  $\mathbb{B}_i$ , as well as  $\mathbb{A}_i$  itself, can send messages to  $\mathbb{A}_i$ . We allow  $\mathbb{A}_i$  to send a message to itself in case  $\mathbb{A}_i$ wants to save it for later use, thus simulating a memory mechanism.

We establish a Curry–Howard correspondence between  $\lambda_{\parallel}$  terms and (fragments of) the intermediate logics obtained from the intuitionistic natural deduction calculus **NJ** by allowing a non-nested use of the rules corresponding to axioms of the form (1). The terms typed using these natural deduction rules contain as many parallel processes as the number of premises. For instance, the decorated version of the rules for the axiom schema  $em = (\mathbb{A} \to \mathbb{A} \land \bot) \lor (\mathbb{B} \to \mathbb{B} \land \mathbb{C})$  and  $C_3 = (\mathbb{A} \to \mathbb{A} \land \mathbb{B}) \lor (\mathbb{B} \to \mathbb{B} \land \mathbb{C}) \lor (\mathbb{C} \to \mathbb{C} \land \mathbb{A})$ , intuitionistically equivalent to the excluded middle law and  $C_3$  [23] are

$$\begin{bmatrix} a:A \to A \land \bot \end{bmatrix} \begin{bmatrix} a:B \to B \land A \end{bmatrix} \qquad \begin{bmatrix} a:A \to A \land B \end{bmatrix} \begin{bmatrix} a:B \to B \land C \end{bmatrix} \begin{bmatrix} a:C \to C \land A \end{bmatrix} \\ \vdots \\ \frac{u:C}{a(u \parallel v):C} \quad (em) \qquad \frac{t:D}{a(t \parallel u \parallel v):D} \quad (C_3)$$

with the (1-depth) restriction: t, u and v are a parallel composition of simply-typed  $\lambda$ -terms that cannot communicate with each other. The variable a represents a private communication channel that behaves similarly to the  $\pi$ -calculus operator  $\nu$ . The typing rules establish how the communication channels connect the terms. For example, (em) encodes the fact that the process v can receive a message of type A from the process u, while the rule  $(C_3)$  that t can receive a message from u, u from v and v from t. The behavior of these channels during the actual communications is defined by the reduction rules of the calculus. If we omit reflexive

 $<sup>^{1}</sup>$ [12, 13] Introduced the concept of *bounded depth proofs* to approximate classical logic with nested applications of the structural rule expressing the excluded middle axiom EM limited by a fixed natural number.

<sup>&</sup>lt;sup>2</sup>Grace stands for GRAph-based Communication in Eden.

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$x^A : A$	$\frac{u:\bot}{u\operatorname{efq}_P:P}  \text{with } P \text{ atomic, } P \neq \bot$		$[x^A : A]$	
$\frac{u:A  t:B}{\langle u,t\rangle:A\wedge B}$	$\frac{u:A \wedge B}{u \pi_0:A}$	$\frac{u:A \wedge B}{u  \pi_1 : B}$	$\frac{u : B}{\lambda x^A u : A \to B}$	$\frac{t:A \to B  u:A}{tu:B}$

Table 1: Type assignments for the simply typed  $\lambda$ -calculus.

edges, the communication topologies corresponding to the reductions for the above rules are



In particular, (em) implements the simplest message-passing mechanism, similar to that of the asynchronous  $\pi$ -calculus [18], and  $(C_3)$  cyclic communication among three processes.

The restriction (1-depth) enables us to define simple and yet expressive reduction rules and to prove the strong normalization of  $\lambda_{\parallel}$ . The resulting calculus is strictly more expressive than the simply typed  $\lambda$ -calculus, and can be used for interesting computational tasks (Sec. 6).

## 2 The Typing System of $\lambda_{\parallel}$

 $\lambda_{\parallel}$  extends the simply typed  $\lambda$ -calculus with channels for multi-party communication and reduction rules for message exchange. Table 1 contains the type assignments for  $\lambda$ -terms, see e.g. [16] for details. Terms typed by such rules are called **simply typed**  $\lambda$ -terms and are denoted here by  $t, u, v \dots$  Terms may contain variables  $x_0^A, x_1^A, x_2^A, \dots$  of type A for every formula A. Free and bound variables of a proof term are defined as usual. We assume the standard renaming rules and  $\alpha$ -equivalences that avoid the capture of variables during reductions.

The typing rules of simply typed  $\lambda$ -calculus, stripped of  $\lambda$ -terms, are the inference rules of Gentzen's natural deduction system **NJ** for **IL**. Actually, if  $\Gamma = x_1 : A_1, \dots, x_n : A_n$ , and all free variables of a term t : A are in  $x_1, \dots, x_n$ , from the logical point of view, t represents an **NJ** derivation of A from the hypotheses  $A_1, \dots, A_n$ . We will thus write  $\Gamma \vdash t : A$ .

**Notation.**  $\rightarrow$  and  $\wedge$  associate to the right.  $\langle t_1, t_2, \ldots, t_n \rangle$  stands for  $\langle t_1, \langle t_2, \ldots, \langle t_{n-1}, t_n \rangle \ldots \rangle \rangle$ , and  $\langle t_1, t_2, \ldots, t_n \rangle \pi_i$ ,  $(i = 0, \ldots, n)$  for  $\langle t_1, t_2, \ldots, t_n \rangle \pi_1 \ldots \pi_1 \pi_0$  containing the projections that select the (i + 1)th element of the sequence.  $\neg A$  is  $A \rightarrow \bot$  and  $\top$  is  $\bot \rightarrow \bot$ .

To type parallel terms that interact according to possibly complex communication mechanisms, we build on ideas from [4, 12, 14, 11] and base the Curry–Howard correspondence for  $\lambda_{\parallel}$  on a fragment of the axiomatic extensions of **IL** with (intuitionistically equivalent versions of) classical disjunctive tautologies [14]. These axioms can be transformed into rules involving parallel communicating sequents (i.e., hypersequents [7]), and as shown in [4] they lead to various communication schemata. The fragment considered in this paper consists of 1-depth proofs, whose notion is adapted from [12, 13]. These are **NJ** proofs with *non-nested* applications of the natural deduction version of the corresponding hypersequent rules, see [11]. The use of this fragment drastically simplifies the reduction rules of  $\lambda_{\parallel}$  w.r.t. the calculi in [4, 2, 3] and enables us to type channels with input/output directions, as in the  $\pi$  calculus.

The class Ax of axiom schemata that we consided here, that will be shown in Sec. 4 to encode all communication topologies represented as reflexive directed graphs, is

 $(\mathbb{A}_1 \to \mathbb{A}_1 \land \mathbb{B}_1) \lor \ldots \lor (\mathbb{A}_m \to \mathbb{A}_m \land \mathbb{B}_m)$ 

where  $\mathbb{A}_i \neq \mathbb{A}_j$  whenever  $i \neq j$ ; and for any  $i \in \{1, \ldots, m\}$ , either  $\mathbb{B}_i = \bot$  or  $\mathbb{B}_i = \mathbb{A}_{k_1} \land \ldots \land \mathbb{A}_{k_p}$ , with  $k_1 < \ldots < k_p$ .

**Remark 1.** Each disjunct  $\mathbb{A}_i \to \mathbb{A}_i \land \mathbb{B}_i$  is logically equivalent to  $\mathbb{A}_i \to \mathbb{B}_i$ . The redundant occurrence of  $\mathbb{A}_i$  is kept to type a memory mechanism for input channels.

$$\begin{array}{c} \displaystyle \frac{t_1:A \quad \dots \quad t_n:A}{t_1 \parallel \dots \parallel t_n:A} \ (contr) \quad \text{ where } t_1,\dots,t_n \text{ are simply typed } \lambda \text{-terms} \\ \\ \displaystyle [\overline{a}^{A_1 \to A_1 \wedge B_1}, \underline{a}^{A_1 \to A_1 \wedge B_1}:A_1 \to A_1 \wedge B_1] \quad & [\overline{a}^{A_m \to A_m \wedge B_m}, \underline{a}^{A_m \to A_m \wedge B_m}:A_m \to A_m \wedge B_m] \\ \\ \quad \vdots \\ \displaystyle \frac{u_1 \parallel \dots \parallel u_n:B \quad \dots \quad u_p \parallel \dots \parallel u_q:B}{A_a((u_1 \parallel \dots \parallel u_n) \parallel \dots \parallel (u_p \parallel \dots \parallel u_q)):B} \ (\mathcal{A}) \\ \\ \text{ where } (A_1 \to A_1 \wedge B_1) \vee \dots \vee (A_m \to A_m \wedge B_m) \text{ is an instance of } \mathcal{A} \in \mathsf{Ax} \end{array}$$

#### Table 2: Type assignments for $\lambda_{\parallel}$ .

As usual, an instance of an axiom schema  $\mathcal{A} \in Ax$  is a formula obtained from  $\mathcal{A}$  by uniformly replacing each propositional variable with an actual formula.  $\lambda_{\parallel}$  is obtained by the Curry–Howard correspondence applied to **NJ** extended with non-nested applications of the natural deduction rules for the axioms in Ax. The type assignment for  $\lambda_{\parallel}$  terms comprises the rules for the simply typed  $\lambda$ -calculus in Table 1 and those for the parallel operators in Table 2. We denote the variables of simply typed  $\lambda$ -calculus as  $x^A$ ,  $y^A$ ,  $z^A$ ,...,  $a^A$ ,  $b^A$ ,  $c^A$ ,...,  $\overline{a}^A$ ,  $\overline{b}^A$ ,  $\overline{c}^A$ ,...,  $\underline{a}^A$ ,  $\underline{b}^A$ ,  $\underline{c}^A$ ,... and, whenever the type is not important, as  $x, y, z, \ldots, a, b, c$ ... We call **intuitionistic variables** the variables  $x, y, z, \ldots$ , which stand for terms; they are bound by the  $\lambda$  operator. The variables  $a, \overline{a}, \underline{a}, \ldots$  are called **channels** or **communication variables** and represent communication channels between parallel processes:  $a, b, c, \ldots$  are used as channel binders,  $\overline{a}, \overline{b}, \overline{c}, \ldots$  represent **output channels** that can transmit messages, while  $\underline{a}, \underline{b}, \underline{c}, \ldots$ **input channels** that can receive messages. We denote  $\overset{A}{a}(u_1 \parallel \ldots \parallel u_m)$  by  $_a(u_1 \parallel \ldots \parallel u_m)$ when  $\mathcal{A}$  is clear from the context or irrelevant. All free occurrences of  $\overline{a}$  and  $\underline{a}$  in  $u_1, \ldots, u_m$ are bound in  $_a(u_1 \parallel \ldots \parallel u_m)$  and must have the types indicated by the inference rule ( $\mathcal{A}$ ).

The rule (contr) is useful for representing parallelism without communication. It is logically redundant, though, since it is an instance of  $(\mathcal{A})$  with no channel occurrence.

From a computational perspective the rules  $(\mathcal{A})$  produce terms of the shape  $\overset{\mathcal{A}}{a}(v_1 \parallel \ldots \parallel v_m)$  that put in parallel  $v_1, \ldots, v_m$ , which we call the **processes** of this term; each  $v_i$  in turn has the shape  $u_1 \parallel \ldots \parallel u_k$ , where  $u_1, \ldots, u_k$  are simply typed  $\lambda$ -terms called the **threads** of  $v_i$ . Processes can communicate through the channel a, whereas their threads represent parallel independent subprograms that cannot interact with each other. Informally, in order to establish a communication channel connecting two terms  $v_i$  and  $v_j$ , we require that  $\overline{a}^{A_i \to A_i \wedge B_i}$  occurs in  $v_i$ ,  $\underline{a}^{A_j \to A_j \wedge B_j}$  occurs in  $v_i$  and  $A_i$  is in  $B_j$ .

On one hand, the argument w of a channel application  $\overline{a}^{A_i \to A_i \wedge B_i} w$  will be interpreted as a message of type  $A_i$  that must be *transmitted*; on the other hand, the channel application  $\underline{a}^{A_j \to A_j \wedge B_j} t$  will *receive* a batch of messages of type  $B_j$  containing w that will replace the whole channel application  $\underline{a}^{A_j \to A_j \wedge B_j} t$  upon reception. This is the reason why the direction of the communication and the direction of  $\to$  are reversed. In general, each channel application  $\overline{a} v$ first transmits v and, immediately after that, starts to listen on the same channel by reducing to  $\underline{a} v$  and waiting for a message that will replace the whole term  $\underline{a} v$ . To formalize the relation between a process  $v_i$  and all the processes  $v_j$  such that  $v_i$  can send messages to  $v_j$ , we need to look at the structure of the axiom schema  $\mathcal{A}$ , because its instances may lose information about its general shape. For this purpose, we introduce the concept of outlink.

**Definition 2** (Outlinks). Let  ${}^{\mathcal{A}}_{a}(v_1 \parallel \ldots \parallel v_m)$  be a term, where  $\mathcal{A} = (\mathbb{A}_1 \to \mathbb{A}_1 \land \mathbb{B}_1) \lor \ldots \lor (\mathbb{A}_m \to \mathbb{A}_m \land \mathbb{B}_m)$ . For any  $i, j \in \{1, \ldots, m\}$  and  $i \neq j$ , we say that the term  $v_i$  is **outlinked** to the term  $v_j$  if  $\mathbb{B}_j = \mathbb{A}_{k_1} \land \ldots \land \mathbb{A}_i \land \ldots \land \mathbb{A}_{k_p}$ .

**Remark 3.** The restriction of (1-depth) proofs forces the derivations of the premises of the rule  $(\mathcal{A})$  to be **IL** proofs; by removing this restriction, the rule is equivalent to an (unrestricted)

instance of the axiom  $\mathcal{A}$ , see [11].

## 3 Communications in $\lambda_{\parallel}$

In Sec. 2 we showed how to install communication channels connecting processes, we present now the reduction rules of  $\lambda_{\parallel}$  that implement the actual communications. Since these communications are higher-order, they can transmit arbitrary simply typed  $\lambda$ -terms as messages, provided their free variables are not bound in the surrounding context. Unlike in [4], messages are thus not restricted to values and communication channels have directions. The reduction rules of  $\lambda_{\parallel}$ comprise two groups of rules: those for the simply typed  $\lambda$ -calculus (*intuitionistic reductions*), and those that deal with process communication (*cross reductions*).

Intuitionistic Reductions. The usual computational rules for the simply typed  $\lambda$ -calculus represent the operations of applying a function to an argument and extracting a component from a pair [16]. From the logical point of view, they are the standard Prawitz' reductions [27] of the natural deduction calculus NJ for IL.

**Cross Reductions.** Their goal is to implement communication, namely to transmit programs in the form of simply typed  $\lambda$ -terms. A very simple example of cross reduction for the (em) topology shown in Section 1 is the following:

 $_{a}(\mathcal{C}[\overline{a} v] \parallel \mathcal{D}[\underline{a} w]) \mapsto _{a}(\mathcal{C}[\underline{a} v] \parallel \mathcal{D}[\langle w, v \rangle])$ 

where C[], D[], v and w contain no channels. In general, since  $\lambda_{\parallel}$  terms may contain more than one output channel application, we first have to choose which application will transmit the next message. For example, here we have two communicating processes, each containing two threads: (\*)  $a((\underline{a} \ r \parallel \overline{a} (x(\overline{a} \ s))) \parallel (\underline{a} \ u \parallel \underline{a} (y(\underline{a} \ w)))))$ 

where r, s, u, w are simply typed  $\lambda$ -terms not containing a. The first process  $\underline{a} r \parallel \overline{a} (x(\overline{a} s))$  contains two occurrences of the output channel  $\overline{a}$ . Let us focus on its second thread  $\overline{a} (x(\overline{a} s))$ . The channel application  $\overline{a} (x(\overline{a} s))$  cannot transmit the message  $x(\overline{a} s)$ , because the channel a might be used with a different type in the second process, so type preservation after reduction would fail. Hence the only possibility here is to choose the second channel application  $\overline{a} s$  as the one containing the output message, in this case s. In general, to make sure that the message does not contain the channel a, it is enough to choose as application occurrence that contains the output message the *rightmost occurrence* of the channel a in the whole process, provided it occurs as an actual output channel  $\overline{a}$ . Hence, in any process, the rightmost thread that contains the channel  $\overline{a}$  may contain the message. If instead the *rightmost occurrence* of a is of the form  $\underline{a}$ , the process has no message to send.

The second choice is which occurrence of an input channel application should receive the current message. For example, in the term (\*) above, the second process  $\underline{a} \ u \parallel \underline{a} (y(\underline{a} w))$  contains three occurrences of  $\underline{a}$ . Since a channel can both send and receive, and has usually sent something before receiving, we are led to choose again the rightmost occurrence of a channel application as receiver, provided it is an input channel. Nonetheless, since the threads of a single process do not communicate with each other, it is best to let all of them receive the message in correspondence of their *locally rightmost* channel application. Thus in the example, both  $\underline{a} \ u$  and  $a \ w$  will receive messages.

The third choice is what to do with the arguments of a receiving channel. After a few programming examples, as those in Section 6, it is natural to convince oneself that it is better to keep the arguments, a feature that we call *memory*. In the previous term, when <u>a</u> u and <u>a</u> w receive s, they will be replaced respectively, with  $\langle u, s \rangle$  and  $\langle w, s \rangle$ .

Continuing our example and summing up, we will have the following reduction of (\*):

 ${}_{a}( \ (\underline{a} \ r \parallel \overline{a} \ (x(\overline{a} \ s))) \ \parallel \ (\underline{a} \ u \parallel \underline{a} \ (y(\underline{a} \ w))) \ ) \quad \mapsto \quad {}_{a}( \ (\underline{a} \ r \parallel \overline{a} \ (x(\underline{a} \ s))) \ \parallel \ (\langle u, s \rangle \parallel \underline{a} \ (y\langle w, s \rangle)) \ )$ 

As we can see, the message s in correspondence of the rightmost occurrence of  $\overline{a}$  in  $\overline{a}(x(\overline{a} s))$  is transmitted by the first process to the rightmost application of  $\underline{a}$  in each one of the two threads  $\underline{a} u$  and  $\underline{a}(y(\underline{a} w))$  of the second process; at the same time, the output channel application  $\overline{a} s$ has turned into the input channel application  $\underline{a} s$ .

The fourth choice to make is which processes should receive messages and which processes should send them. As anticipated in the previous section, we are guided by the typing. Let us consider for instance the term  $_a(x(\underline{a}^{A\to A\wedge B}s) \parallel y(\overline{a}^{B\to B\wedge A}t))$ , where s and t are specific simply typed  $\lambda$ -terms not containing a, while  $x : A \wedge B \to C$  and  $y : B \wedge A \to C$ . A cross reduction rule corresponding to this typing rule admits communication in two directions, from left to right and from right to left, because, according to the types, the second process can receive messages from the first and viceversa. The actual direction of the message is determined however by the rightmost occurrences of the channel a in the two processes. In this case, the rightmost occurrence of a in the first process is an input channel, while the rightmost occurrence of a in second process is an output channel. Hence the reduction is from right to left and is  $_a(x(\underline{a}^{A\to A\wedge B}s) \parallel y(\overline{a}^{B\to B\wedge A}t)) \mapsto _a(x(s,t) \parallel y(\underline{a}^{B\to B\wedge A}t))$ 

To define communication reductions, we need to introduce two kinds of contexts: one for terms that can communicate, one for terms which are in parallel but cannot communicate.

**Definition 4** (Simple Parallel Term). A simple parallel term is a  $\lambda_{\parallel}$  term  $t_1 \parallel \ldots \parallel t_n$ , where each  $t_i$ , for  $1 \leq i \leq n$ , is a simply typed  $\lambda$ -term.

**Definition 5.** A context C[ ] is a  $\lambda_{\parallel}$  term with some fixed variable [] occurring

- A simple context is a context which is a simply typed  $\lambda$ -term.
- A simple parallel context is a context which is a simple parallel term.

For any  $\lambda_{\parallel}$  term u of the same type of [], C[u] denotes the term obtained replacing [] with u in C[], without renaming bound variables.

We explain the general case of the cross reduction. The rule identifies a single process as the receiver and possibly several processes as senders. Once the receiving process is fixed, the senders are determined by the axiom schema  $\mathcal{A}$ , that is instantiated by the type of the communication channel occurring in the receiving process. In particular, in the term

(\*)  $A_{\underline{a}}(... \| C_1[\overline{a} w_1] \|... \| (... \| D_1[\underline{a} v_1] \|... \| D_n[\underline{a} v_n] \|...) \|... \| C_p[\overline{a} w_p] \|...)$ the processes  $C_1[\overline{a} w_1], \ldots, C_p[\overline{a} w_p]$  are the senders and  $(... \| D_1[\underline{a} v_1] \|... \| D_n[\underline{a} v_n] \|...)$  in its entirety is the receiver. Formally,  $C_1[\overline{a} w_1], \ldots, C_p[\overline{a} w_p]$  are all the process *outlinked* to the process  $(... \| D_1[\underline{a} v_1] \|... \| D_n[\underline{a} v_n] \|...)$ , see Definition 2. In this latter process, we have displayed the threads that actually contain the channel a: all of them will receive the messages. Consistently with our choices, the displayed occurrences of a are rightmost in each  $D_j[\underline{a} v_j]$  and in each  $C_j[\overline{a} w_j]$ . The processes containing  $w_1, \ldots, w_p$  send them to all the rightmost occurrences of  $\underline{a} v_1, \ldots, \underline{a} v_n$  in the processes  $D_1, \ldots, D_n$ :

 $(\star) \mapsto \overset{A}{a}(...\|\mathcal{C}_1[aw_1]\|...\|(...\|\mathcal{D}_1[\langle v_1, w_1, ..., w_p\rangle]\|...\|\mathcal{D}_n[\langle v_n, w_1, ..., w_p\rangle]\|...\|\mathcal{C}_p[aw_p]\|...)$ provided that, for each  $w_j$ , its free variables are free in  $\mathcal{C}_j[aw_j]$ : this condition is needed to avoid that bound variables become free, violating the Subject Reduction. Whenever  $w_j$  is a closed term – executable code – the condition is automatically satisfied. As we can see, the reduction retains all terms  $v_1, \ldots, v_n$  occurring in  $\mathcal{D}_1, \ldots, \mathcal{D}_n$  before the communication.

**Remark 6.** Unlike in  $\pi$ -calculus, channel occurrences that send messages are not immediately consumed in  $\lambda_{\parallel}$ , because cross reductions adopt the perspective of the receiver rather than that of the senders. However, after transmission, the output channel occurrence will be turned

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Intuitionistic Reductions  $(\lambda x^A u)t \mapsto u[t/x^A] \quad \langle u_0, u_1 \rangle \pi_i \mapsto u_i \text{ for } i = 0, 1$ 

Cross Reductions: Communication

 $\begin{array}{c} \mathcal{A}_{a}(...\|\ \mathcal{C}_{1}[\overline{a}\ w_{1}]\ \|...\|(...\|\ \mathcal{D}_{1}[\underline{a}\ v_{1}]\ \|...\|\ \mathcal{D}_{n}[\underline{a}\ v_{n}]\ \|...)\|...\|\ \mathcal{C}_{p}[\overline{a}\ w_{p}]\ \|...) \mapsto \\ \mathcal{A}_{a}(...\|\ \mathcal{C}_{1}[\underline{a}\ w_{1}]\ \|...\|(...\|\ \mathcal{D}_{1}[\langle v_{1}, w_{1}, ..., w_{p}\rangle]\ \|...\|\ \mathcal{D}_{n}[\langle v_{n}, w_{1}, ..., w_{p}\rangle]\ \|...\|\ \mathcal{C}_{p}[\underline{a}\ w_{p}]\ \|...) \mapsto \\ \mathcal{A}_{a}(...\|\ \mathcal{C}_{1}[\underline{a}\ w_{1}]\ \|...\|(...\|\ \mathcal{D}_{1}[\langle v_{1}, w_{1}, ..., w_{p}\rangle]\ \|...\|\ \mathcal{D}_{n}[\langle v_{n}, w_{1}, ..., w_{p}\rangle]\ \|...\|\ \mathcal{C}_{p}[\underline{a}\ w_{p}]\ \|...) \mapsto \\ \mathcal{A}_{a}(...\|\ \mathcal{C}_{1}[\underline{a}\ w_{1}]\ \|...\|(...\|\ \mathcal{D}_{1}[\langle v_{1}, w_{1}, ..., w_{p}\rangle]\ \|...\|\ \mathcal{D}_{n}[\langle v_{n}, w_{1}, ..., w_{p}\rangle]\ \|...\|\ \mathcal{C}_{p}[\underline{a}\ w_{p}]\ \|...\|$ 

where  $C_1[\overline{a} \ w_1], \ldots, C_p[\overline{a} \ w_p]$  are all the processes outlinked to the process  $(\ldots \parallel \mathcal{D}_1[\underline{a} \ v_1] \parallel \ldots \parallel \mathcal{D}_n[\underline{a} \ v_n] \parallel \ldots);$ the displayed occurrences of a are rightmost in each  $\mathcal{D}_j[\underline{a} \ v_j]$  and in each  $\mathcal{C}_j[\overline{a} \ w_j];$  each  $\mathcal{D}_j$  is a simple context; the free variables of each  $w_j$  are free in  $\mathcal{C}_j[\overline{a} \ w_j];$ 

 $\begin{array}{c} \textbf{Cross Reductions: Simplification} \quad \begin{array}{c} \overset{\mathcal{A}}{a}((u_1 \parallel \ldots \parallel u_n) \parallel \ldots \parallel (u_m \parallel \ldots \parallel u_p)) \mapsto u_{i_1} \parallel \ldots \parallel u_{i_q} \\ \text{whenever } u_{i_1}, \ldots, u_{i_q} \text{ do not contain } a \text{ and } 1 \leq i_1 < \ldots < i_q \leq p. \end{array}$ 

Table 3: Reduction Rules for  $\lambda_{\parallel}$ .

immediately into an input channel occurrence, which will be consumed when the process is selected as current receiver.

The last choice is what to do with the threads or processes that do not contain any communication channel. The idea is that whenever a term contains no channel occurrence, it has already reached a result, as it does not need to interact with the context. Hence at the end of the computation we select some of the processes that have reached their own results and consider them all together the global result of the computation. Thus we introduce the simplification reduction in Table 3, which also displays all the reduction rules of  $\lambda_{\parallel}$ . As usual, we adopt the reduction schema:  $C[t] \mapsto C[u]$  whenever  $t \mapsto u$  and for any context C. We denote by  $\mapsto^*$  the reflexive and transitive closure of the one-step reduction  $\mapsto$ .

**Theorem 7** (Subject Reduction). If t : A and  $t \mapsto u$ , then u : A and all the free variables of u appear among those of t.

*Proof.* The only not trivial case is that of cross reductions. Assume the step is as follows:

$$\begin{array}{c} \overset{\mathcal{A}}{a}(... \| \ \mathcal{C}_{1}[\overline{a} \ w_{1}] \ \|... \| \ (... \| \ \mathcal{D}_{1}[\underline{a}^{\ A_{i} \to A_{i} \land B_{i}} \ v_{1}] \ \|... \| \ \mathcal{D}_{n}[\underline{a}^{\ A_{i} \to A_{i} \land B_{i}} \ v_{n}] \ \|... \| \\ \mapsto \\ \overset{\mathcal{A}}{a}(... \| \ \mathcal{C}_{1}[\underline{a} \ w_{1}] \ \|... \| \ (... \| \ \mathcal{D}_{1}[\langle v_{1}, w_{1}, ..., w_{p} \rangle] \ \|... \| \ \mathcal{D}_{n}[\langle v_{n}, w_{1}, ..., w_{p} \rangle] \ \|... \| \\ \mathcal{C}_{p}[\underline{a} \ w_{p}] \ \|... ) \\ \end{array}$$

As the type of the channel a is an instance  $(A_1 \to A_1 \land B_1) \lor \ldots \lor (A_m \to A_m \land B_m)$  of the schema  $\mathcal{A} = (\mathbb{A}_1 \to \mathbb{A}_1 \land \mathbb{B}_1) \lor \ldots \lor (\mathbb{A}_m \to \mathbb{A}_m \land \mathbb{B}_m)$  where  $\mathbb{B}_i = \mathbb{A}_{k_1} \land \ldots \land \mathbb{A}_{k_p}$ , and as  $w_1 : A_{k_1}, \ldots, w_p : A_{k_p}$  where  $A_{k_1}, \ldots, A_{k_p}$  instantiate  $\mathbb{A}_{k_1}, \ldots, \mathbb{A}_{k_p}$ , then  $(\ldots \parallel \mathcal{D}_1[\langle v_1, w_1, \ldots, w_p \rangle] \parallel \ldots \parallel \mathcal{D}_n[\langle v_n, w_1, \ldots, w_p \rangle] \parallel \ldots)$  is well defined and its type is the same as that of  $(\ldots \parallel \mathcal{D}_1[\underline{a}^{A_i \to A_i \land B_i} v_1] \parallel \ldots \parallel \mathcal{D}_n[\underline{a}^{A_i \to A_i \land B_i} v_n] \parallel \ldots)$ . Hence the term type term does not change.

Since the displayed occurrences of a are rightmost in each  $C_j[\overline{a} \ w_j]$  and thus a does not occur in  $w_j$ , no occurrence of a with type different from  $A_i \to A_i \wedge B_i$  occurs in the terms  $\mathcal{D}_1[\langle v_1, w_1, \dots, w_p \rangle], \dots, \mathcal{D}_n[\langle v_n, w_1, \dots, w_p \rangle]$ . Finally, the free variables of each  $w_j$  are free also in  $C_j[\overline{a} \ w_j]$ , and thus no new free variable is created by the reduction.

**Remark 8.** The communication reductions of  $\lambda_{\parallel}$  allow processes to communicate independently of their order, and hence the parallel composition needs not to be commutative.

## 4 From Communication Topologies to Programs

We present a method for automatically extracting  $\lambda_{\parallel}$  typing rules from graph-specified communication topologies. Given a directed, reflexive graph G whose nodes and edges represent

respectively processes and communication channels, we describe how to transform it into an axiom schema  $\mathcal{A} \in Ax$  corresponding to a typing  $(\mathcal{A})$  rule for  $\lambda_{\parallel}$  terms. We will show that this typing rule encodes a process topology which exactly mirrors the graph G: two processes may communicate if and only the corresponding graph nodes are connected by an edge and the direction of communication follows the edge. In other words, the edges of the graph correspond to the *outlinked* relation between processes of Definition 2.

**Procedure 1.** Given a directed reflexive graph G = (V, E) with k = |V|, the axiom schema  $\mathcal{A}$  encoding G is  $\mathbb{C}_1 \vee \cdots \vee \mathbb{C}_k$  such that for each  $n \in \{1, \ldots, k\}$ :

- $\mathbb{C}_n = \mathbb{A}_n \to \mathbb{A}_n \land \mathbb{A}_{i_1} \land \ldots \land \mathbb{A}_{i_m}$ , if the n-th node in G has incoming edges from the non-empty list of pairwise distinct nodes  $i_1, \ldots, i_m \neq n$ ;
- $\mathbb{C}_n = \mathbb{A}_n \to \mathbb{A}_n \land \bot$ , if the *n*-th node in *G* has only one incoming edge.

We show an example and then state the correspondence between graphs and  $\lambda_{\parallel}$  reductions.

**Example 9.** Consider the axiom below corresponding to the graph



 $(\mathbb{A}_1 \to \mathbb{A}_1 \land \mathbb{A}_2 \land \mathbb{A}_4) \lor (\mathbb{A}_2 \to \mathbb{A}_2 \land \mathbb{A}_1) \lor (\mathbb{A}_3 \to \mathbb{A}_3 \land \mathbb{A}_1 \land \mathbb{A}_2) \lor (\mathbb{A}_4 \to \mathbb{A}_4 \land \bot)$ 

The rule extracted from a graph forces communications to happen as indicated by the edges of the graph. Indeed the following holds.

**Proposition 10** (Topology Correspondence). For any directed reflexive graph G and term  $t := {}_{a}(u_1 \parallel \ldots \parallel u_m)$  typed using the rule corresponding to the axiom extracted from G by Procedure 1, there is a cross reduction for t that transmits a term w from  $u_x$  to  $u_y$  if and only if G contains an edge from x to y.

### 5 The Strong Normalization Theorem

We prove the strong normalization theorem for  $\lambda_{\parallel}$ : any reduction of every  $\lambda_{\parallel}$  term ends in a finite number of steps into a normal form. This means that the computation of any typed  $\lambda_{\parallel}$  term always terminates independently of the chosen reduction strategy. Instead of struggling directly with the complicated combinatorial properties of process communication, as done for the weak normalization in [3, 2], we reduce the strong normalization of  $\lambda_{\parallel}$  to the strong normalization of a non-deterministic reduction relation over simply typed  $\lambda$ -terms. The idea is to simulate communication by non-determinism, a technique inspired by [6].

Definition 11 (Normal Forms and Strongly Normalizable Terms).

- A redex is a term u such that u → v for some v and reduction in Table 3. A term t is called a normal form or, simply, normal, if t is not a redex.
- A finite or infinite sequence of terms u<sub>1</sub>, u<sub>2</sub>,..., u<sub>n</sub>,... is said to be a reduction of t, if t = u<sub>1</sub>, and for all i, u<sub>i</sub> → u<sub>i+1</sub>. A term u of λ<sub>||</sub> is normalizable if there is a finite reduction of u whose last term is normal and is strong normalizable if every reduction of u is finite. With SN we denote the set of the strongly normalizable terms of λ<sub>||</sub>.

#### 5.1 Non-deterministic Reductions

Strong normalization for parallel  $\lambda$ -calculi is an intricate problem. Decreasing complexity measures are quite hard to find and indeed those introduced in [3, 2] for proving normalization of fragments of  $\lambda_{\parallel}$  fail in case of strong normalization. Given a term  $_a(u_1 \parallel \ldots \parallel u_n)$ , one would like to measure its complexity as a function of the complexities of the terms  $u_i$ , for example taking into account the number of channel occurrences and the length of the longest reduction of  $\lambda$ -redexes in  $u_i$ . However, when  $u_i$  receives a message its code may drastically change and both those numbers may increase. Moreover, there is a potential circularity to address: channels send messages and may generate new  $\lambda$ -calculus redexes in the receivers;  $\lambda$ -calculus redexes may duplicate channel occurrences, generating even more communications.

To break this circularity, we use a radically different complexity measure. The complexity of a process  $u_i$  should take into account all the possible messages that  $u_i$  may receive. If  $u_i = \mathcal{D}[\underline{a} \ t]$ , after receiving a message it becomes  $\mathcal{D}[\langle t, s \rangle]$ , where s is an *arbitrary* simply typed  $\lambda$ -term from the point of view of  $u_i$ . We thus create a reduction relation over simply typed  $\lambda$ -terms that simulates the reception "out of the blue" of this kind of messages. Namely, we extend the reduction relation of  $\lambda$ -calculus by two new rules: i)  $\overline{a}^T \rightsquigarrow \underline{a}^T$ ; ii)  $\underline{a}^T \rightsquigarrow t$  for every channel  $a^T$  and simply typed  $\lambda$ -term t: T that does not contain channels. In order to simulate the reception of an arbitrary batch w of messages, t will be instantiated as  $\lambda x \langle x, w \rangle$  in the proof of Th. 24. With these reductions,  $\lambda$ -terms that do not contain channels are the usual deterministic ones.

**Definition 12** (Deterministic simply typed  $\lambda$ -terms). A simply typed  $\lambda$ -term t is called **deterministic**, if t does not contain any channel occurrence.

**Definition 13** (The non-deterministic reduction relation  $\rightsquigarrow$ ). The reduction relation  $\rightsquigarrow$  over simply typed  $\lambda$ -terms is defined as extension of the relation  $\mapsto$  as follows:

 $(\lambda x^A u) t \rightsquigarrow u[t/x^A] \qquad \langle u_0, u_1 \rangle \pi_i \rightsquigarrow u_i, \text{ for } i = 0, 1 \qquad \overline{a}^T \rightsquigarrow \underline{a}^T$ 

 $\underline{a}^T \rightsquigarrow t$ , for every channel  $a^T$  and deterministic simply typed  $\lambda$ -term t: T

and as usual we close by contexts:  $C[t] \rightsquigarrow C[u]$  whenever  $t \rightsquigarrow u$  and C[] is a simple context. We denote by  $\rightsquigarrow^*$  the reflexive and transitive closure of the one-step reduction  $\rightsquigarrow$ .

The plan of our proof will be to prove the strong normalization of simply typed  $\lambda$ -calculus with respect to the reduction  $\rightsquigarrow$  (Corollary 23) and then derive the strong normalization of  $\lambda_{\parallel}$  using  $\rightsquigarrow$  as the source of the complexity measure (Theorem 22). The first result will be proved by the standard Tait–Girard reducibility technique (Definition 14).

We define  $SN^*$  to be the set of strongly normalizing simply typed  $\lambda$ -terms with respect to the non-deterministic reduction  $\rightsquigarrow$ . The reduction tree of a strongly normalizable term with respect to  $\rightsquigarrow$  is no more finite, but still well-founded. It is well-known that it is possible to assign to each node of a well-founded tree an ordinal number, in such a way that it decreases passing from a node to any of its children. We will call the *ordinal size* of a term  $t \in SN^*$  the ordinal number assigned to the root of its reduction tree and we denote it by h(t); thus, if  $t \rightsquigarrow u$ , then h(t) > h(u). To fix ideas, one may define  $h(t) := \sup\{h(u) + 1 \mid t \rightsquigarrow u\}$ .

#### 5.2 Reducibility and Properties of Reducible Terms

We define a notion of reducibility for simply typed  $\lambda$ -terms with respect to the reduction  $\rightsquigarrow$ . As usual, we prove that every reducible term is strongly normalizable w.r.t.  $\rightsquigarrow$  and afterwards that all simply typed  $\lambda$ -terms are reducible. The difference with the usual reducibility proof is that we first prove that deterministic simply typed  $\lambda$ -terms are reducible (Th. 19), which makes it

possible to prove that channels are reducible (Prop. 21) and finally that all terms are reducible (Th. 22). This amounts to prove the usual Adequacy Theorem twice. This stratification of the proof reminds of the reducibility technique employed in [1] for proving weak normalization of a concurrent  $\lambda$ -calculus with shared memory.

**Definition 14** (Reducibility). Assume t : C is a simply typed  $\lambda$ -term. We define the relation  $t \neq C$  ("t is reducible of type C") by induction and by cases according to the form of C: 1.  $t \neq P$ , with P atomic, if and only if  $t \in SN^*$ 

**2.**  $t \mathrel{\mathsf{r}} A \land B$  if and only if  $t \pi_0 \mathrel{\mathsf{r}} A$  and  $t \pi_1 \mathrel{\mathsf{r}} B$ 

3. t r  $A \rightarrow B$  if and only if for all u, if u r A, then tu r B

We show that the set of reducible terms for a formula C is a reducibility candidate [16]. Neutral terms are defined as terms that are not "values" and need to be further reduced.

**Definition 15.** A simply typed  $\lambda$ -term not of the form  $\lambda x u$  or  $\langle u, t \rangle$  is neutral.

**Definition 16** (Reducibility Candidates). Extending the approach of [16], we define three properties of reducible terms t: (CR1) If t r A, then  $t \in SN^*$ , (CR2) If t r A and  $t \rightsquigarrow^* t'$ , then t' r A, and (CR3) If t is neutral and for every t',  $t \rightsquigarrow t'$  implies t' r A, then t r A.

We show that every term t possesses the reducibility candidate properties. The arguments for (CR1), (CR2), (CR3) are standard (see [16]).

**Proposition 17** ([16]). Every simply typed  $\lambda$ -term satisfies (CR1), (CR2), (CR3).

The next task is to prove that all introduction rules of simply typed  $\lambda$ -calculus define a reducible term from a list of reducible terms for all premises.

**Proposition 18.** (1) If for every t r A, u[t/x] r B, then  $\lambda x u$  r  $A \rightarrow B$  and (2) If u r A and v r B, then  $\langle u, v \rangle$  r  $A \wedge B$ .

*Proof.* As in [16], using (CR1), (CR2) and (CR3).

#### 5.3 The Mini Adequacy Theorem

We prove that simply typed  $\lambda$ -terms that do not contain channels are reducible.

**Theorem 19** (Mini Adequacy Theorem). Suppose that w : A is a deterministic simply typed  $\lambda$ -term, with intuitionistic free variables among  $x_1 : A_1, \ldots, x_n : A_n$ . For all terms  $t_1, \ldots, t_n$  such that for  $i = 1, \ldots, n$ ,  $t_i$  r  $A_i$  we have  $w[t_1/x_1 \cdots t_n/x_n]$  r A

*Proof.* As the proof of Th. 22 without case 2., which concerns channels.

**Corollary 20** (Mini Strong Normalization of  $\rightsquigarrow$ ). If t : A is a deterministic simply typed  $\lambda$  term, then  $t \in SN^*$ .

*Proof.* Assume  $x_1 : A_1, \ldots, x_n : A_n$  are all the intuitionistic free variables of t and thus all its free variables. By **(CR3)**, one has  $x_i \neq A_i$ , for  $i = 1, \ldots, n$ . From Theorem 19, we derive  $t \neq A$ . From **(CR1)**, we conclude that  $t \in SN^*$ .

By the Mini Adequacy Theorem we can prove that channels are reducible.

**Proposition 21** (Reducibility of Channels). *i)* For every input channel  $\underline{a} : T$ ,  $\underline{a} \in T$ , and *ii)* For every output channel  $\overline{a} : T$ ,  $\overline{a} \in T$ 

*Proof.* i) Since  $\underline{a}$  is neutral, by (CR3) it is enough to show that for all u such that  $\underline{a} \rightsquigarrow u$ , it holds that  $u \neq T$ . Indeed, let us consider any u such that  $\underline{a} \rightsquigarrow u$ ; since u must be a deterministic simply typed  $\lambda$ -term, by Corollary 20  $u \neq T$ .

ii) Since  $\overline{a}$  neutral, by (CR3) it is enough to show that for all u such that  $\overline{a} \rightsquigarrow u$ , it holds  $u \neq T$ . Indeed, let us consider any u such that  $\overline{a} \rightsquigarrow u$ ; since  $u = \underline{a}$ , by i) we have  $u \neq T$ .

We can finally prove that all simply typed  $\lambda$ -terms are reducible.

**Theorem 22** (Adequacy Theorem). Suppose that w : A is any simply typed  $\lambda$ -term, with intuitionistic free variables among  $x_1 : A_1, \ldots, x_n : A_n$ . For all terms  $t_1, \ldots, t_n$  such that for  $i = 1, \ldots, n$ ,  $t_i \in A_i$ 

$$we\ have \quad w[t_1/x_1\cdots t_n/x_n]$$
r  $A$ 

*Proof.* Notation: for any term v and formula B, we denote

$$v[t_1/x_1\cdots t_n/x_n]$$

with  $\vec{v}$ . We proceed by induction on the shape of w.

- 1. If  $w = x_i : A_i$ , for some *i*, then  $A = A_i$ . So  $\vec{w} = t_i$  r  $A_i = A$ .
- 2. If  $w = a : A_i$ , for some *i* and channel *a*, then  $A = A_i$ . By Prop. 21,  $\vec{a} = a r A$ .
- 3. If w = ut, then  $u : B \to A$  and t : B. So  $\vec{w} = \vec{u} \vec{t} \cdot \mathbf{r} A$ , for  $\vec{u} \cdot \mathbf{r} B \to A$  and  $\vec{t} \cdot \mathbf{r} B$  by induction hypothesis.
- 4. If  $w = \lambda x^B u$ , then  $A = B \to C$ . So,  $\vec{w} = \lambda x^B \vec{u}$ , since we may assume  $x^B \neq x_1, \ldots, x_k$ . For every  $t \in B$ , by induction hypothesis on u,  $\vec{u}[t/x] \in C$ . Hence, by Proposition 18,  $\lambda x^B \vec{u} \in B \to C = A$ .
- 5. The cases of pairs and projections are straightforward.

**Corollary 23** (Strong Normalization of  $\rightsquigarrow$ ). For any simply typed  $\lambda$ -term t : A, then  $t \in SN^*$ .

*Proof.* Let  $x_1 : A_1, \ldots, x_n : A_n$  be all the intuitionistic free variables of t. (CR3) leads to  $x_i r A_i$ . From Th. 22, we derive t r A.  $t \in SN^*$  follows from (CR1).

**Theorem 24** (Strong Normalization of  $\lambda_{\parallel}$ ). For any  $\lambda_{\parallel}$  term  $t, t \in SN$ .

Proof. Assume  $t = {}_{a}(u_{1} \parallel \ldots \parallel u_{m})$  and  $u_{1} = v_{1} \parallel \ldots \parallel v_{n}, \ldots, u_{m} = v_{p} \parallel \ldots \parallel v_{q}$ . Define  $\rho_{i}$ , for  $i \in \{1, \ldots, q\}$ , as the ordinal size  $h(v_{i})$  of  $v_{i}$  with respect to the reduction relation  $\rightsquigarrow$ . We proceed by lexicographic induction on the sequence  $\rho = (\rho_{1}, \ldots, \rho_{q})$ , which we call the complexity of t. Let t' be any term such that  $t \mapsto t'$ : to prove the thesis, it is enough to show that  $t' \in SN$ . Let  $\rho'$  the complexity of t'. We must consider three cases.

**1.**  $t' = {}_{a}(u_{1} \parallel \ldots \parallel u'_{k} \parallel \ldots \parallel u_{m})$  and in turn  $u'_{k} = v_{i} \parallel \ldots \parallel v'_{r} \parallel \ldots \parallel v_{j}$  with  $v_{r} \mapsto v_{r'}$  by contraction of an intuitionistic redex. Then also  $v_{r} \rightsquigarrow v'_{r}$ . Hence  $\rho'$  is lexicographically strictly smaller than  $\rho$  and we conclude by the i.h. that  $t' \in SN$ .

**2.**  $t' = {}_{a}(u'_{1} \parallel ... \parallel u'_{k} \parallel ... \parallel u'_{m})$   $u_{k} = ... \parallel v_{i} \parallel ... \parallel v_{j} \parallel ... u'_{k} = ... \parallel v'_{i} \parallel ... \parallel v'_{j} \parallel ...$  $v_{i} = \mathcal{D}_{1}[\underline{a} \ w_{1}], ..., v_{j} = \mathcal{D}_{n}[\underline{a} \ w_{n}]$   $v'_{i} = \mathcal{D}_{1}[\langle w_{1}, w \rangle], ... v_{j} = \mathcal{D}_{n}[\langle w_{n}, w \rangle]$ 

where w is the sequence of messages transmitted by cross reduction. Being for each l

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 $\mathcal{D}_{l}[\underline{a} \ w_{l}] \rightsquigarrow \mathcal{D}_{l}[(\lambda x \langle x, w \rangle) \ w_{l}] \rightsquigarrow \mathcal{D}_{l}[\langle w_{l}, w \rangle]$ we obtain  $v_{i} \rightsquigarrow^{*} v'_{i}, \ldots v_{j} \rightsquigarrow^{*} v'_{j}$ . Moreover, for each  $l \neq k$ , either  $u_{l} = u'_{l}$  or  $u_{l} = \ldots \parallel s_{i} \parallel \ldots \parallel s_{j} \parallel \ldots \quad u'_{k} = \ldots \parallel s_{i} \parallel \ldots \parallel s'_{j} \parallel \ldots \quad s_{j} = \mathcal{C}[\overline{a} \ r] \rightsquigarrow s'_{j} = \mathcal{C}[\underline{a} \ r]$ Hence  $\rho'$  is lexicographically strictly smaller than  $\rho$  and by i.h.  $t' \in \mathsf{SN}$ . **3.**  $t' = v_{i_{1}} \parallel \ldots \parallel v_{i_{k}}$ . As  $v_{i_{1}}, \ldots, v_{i_{k}}$  all belong to SN, we easily obtain  $t' \in \mathsf{SN}$ .

## Computing with $\lambda_{\parallel}$

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We illustrate the expressive power of  $\lambda_{\parallel}$  with examples of parallel programs from [21].

Reductions are performed according to the principles (a)–(c) below, that make programming in  $\lambda_{\parallel}$  as efficient and deterministic as possible. Indeed, as put by Harper [17], whereas concurrency is concerned with nondeterministic composition of programs, parallelism is concerned with asymptotic *efficiency* of programs with *deterministic behavior*.

(a) Messages should be normalized before being sent. This property is fundamental for efficient parallel computation. Indeed, for instance, a repeatedly forwarded message can be duplicated many times inside a process network; hence if the message is not normal, each process may have to normalize it, wasting resources. This implies in particular that message transmission cannot be triggered according to a call-by-value strategy.

(b) The senders and the receiver of the message should be normalized before the communication. Indeed, the communication behaviour depends on the syntactic shape of a process: it is the rightmost occurrence of the channel in a  $\lambda_{\parallel}$  term to determine what message is sent or received. Hence, a term might send a message, whereas its normal form another. For example  $(\lambda x y x (an))(am)$  would transmit m, while its normal form y (am) (an) would transmit n. We want to avoid this kind of unpredictable behaviour due to the non-confluence of the calculus and make clear what channel is supposed to be the active one, which sends or receives a message.

(c) When no communication nor intuitionistic reduction is possible, we extract the desired result of the computation by a suitable simplification reduction.

Informally, our normalization strategy consists in iterating the basic reduction relation  $\succ$  defined below, that takes a term  $t = a(u_1 \parallel \ldots \parallel u_m)$  and performs the following operations:

1) We select all the threads of  $u_1, \ldots, u_m$  that will send or receive the next message and we let them complete their internal computations.

2) We let the selected threads transmit their message.

3) While executing 1) and 2), to avoid inactivity we let the other threads perform some other independent calculations that may be carried out in parallel.

4) If the previous operations are not possible, we extract the results, if any.

**Definition 25** (Reduction Strategy  $\succ$ ). Let  $t = {}_a(u_1 \parallel ... \parallel u_m)$  be a  $\lambda_{\parallel}$  term. We write  $t \succ t'$  whenever t' has been obtained from t by applying on of the following:

1. We select a receiver  $u_i$  for the next communication. We normalize, among the threads that contain a, those that are rightmost in  $u_i$  or in a process outlinked to  $u_i$ . If now it is possible, we apply a cross reduction

 $_{a}(u_{1} \parallel \ldots \parallel u_{i} \parallel \ldots \parallel u_{m}) \mapsto _{a}(u_{1} \parallel \ldots \parallel u_{i}' \parallel \ldots \parallel u_{m})$ 

followed by some intuitionistic reductions.

2. Provided that by 1. we can only obtain the trivial reduction  $t \mapsto^* t$ , we apply, if possible, a simplification reduction. We then normalize the remaining simply typed  $\lambda$ -terms.

We can reduce every  $\lambda_{\parallel}$ -term in normal form just by iterating the reduction relation  $\succ$ . Indeed, if a communication is possible, by 1. we can select a suitable receiver and apply a cross reduction. If no communication is possible but a simplification can be done, 2. applies and we can simplify the term. Otherwise, we can just normalize the simply typed  $\lambda$ -terms by 1. or 2.

This normalization strategy leaves some room for non-determinism: it prescribes when a communication reduction should be fired, but does not select a process out of those that can potentially receive messages, thus leaving a number of possible ways of actually performing the communication. To limit this non-determinism, before the beginning of the computation we select one process and we impose that only the selected process can receive messages. Immediately after the reception of the messages, we select the next process to the right, if any, or the first process from left otherwise. Another source of non-determinism is due to cross reductions of the form

$${}^{\mathcal{A}}_{a}((u_{m_{1}} \parallel \ldots \parallel u_{n_{1}}) \parallel \ldots \parallel (u_{m_{p}} \parallel \ldots \parallel u_{n_{p}})) \mapsto u_{j_{1}} \parallel \ldots \parallel u_{j_{d}}$$

In this case, we impose that  $u_{j_1} \parallel \ldots \parallel u_{j_q}$  results by selecting from each  $(u_{m_i} \parallel \ldots \parallel u_{n_i})$ , with  $1 \leq i \leq p$ , the leftmost thread not containing a.

Before presenting some examples of parallel programs in  $\lambda_{\parallel}$ , we show that  $\lambda_{\parallel}$  is more expressive than simply typed  $\lambda$  calculus.

**Example 26** (Parallel OR). Berry's sequentiality theorem [8] implies that there is no simply typed  $\lambda$ -term O: Bool  $\rightarrow$  Bool  $\rightarrow$  Bool such that O ff ff  $\mapsto^*$  ff, O utt  $\mapsto^*$  tt, O tt  $u \mapsto^*$  tt for every  $\lambda$ -term u, where tt, ff are the boolean constants. As a consequence, there cannot be a  $\lambda$ -term O such that  $O[\text{ff}/x][\text{ff}/y] \mapsto^*$  ff,  $O[u/x][\text{tt}/y] \mapsto^*$  tt,  $O[\text{tt}/x][u/y] \mapsto^*$  tt for every  $\lambda$ -term u. To implement a  $\lambda_{\parallel}$ -term with the above property we process the two inputs in parallel. If both inputs evaluate to ff, though, at least one process needs to have all the information in order to output the result ff. Hence the simple topology  $\bigcirc$  is enough. Proc. 1 extracts from this graph the axiom schema ( $\mathbb{A} \rightarrow \mathbb{A} \land \bot$ )  $\lor$  ( $\mathbb{B} \rightarrow \mathbb{B} \land \mathbb{A}$ ) with reduction

 ${}_{a}(\mathcal{C}[\overline{a}\,w] \parallel (\mathcal{D}_{1}[\underline{a}\,v_{1}] \parallel ... \parallel \mathcal{D}_{n}[\underline{a}\,v_{n}])) \ \mapsto \ {}_{a}(\mathcal{C}[\underline{a}\,w] \parallel (\mathcal{D}_{1}[\langle v_{1},w \rangle] \parallel ... \parallel \mathcal{D}_{n}[\langle v_{n},w \rangle]))$ 

We add to  $\lambda_{\parallel}$  the boolean type, **tt**, **ff** and an if\_then\_else\_ construct [16]. We define in  $\lambda_{\parallel}$ O :=  $_a$ (if x then tt else  $\bar{a}$  **ff**  $\pi_0 \parallel$  if y then tt else  $\underline{a}$  ( $\lambda x^{\perp} x$ )  $\pi_1$ )

where we assume  $\overline{a} : \mathsf{Bool} \to \mathsf{Bool} \land \bot$  in the first process and that  $\underline{a} : \top \to \top \land \mathsf{Bool}$  in the second one. Now, on one hand

 $O[u/x][tt/y] = {}_{a}(\text{if } u \text{ then } tt \text{ else } \overline{a} \ \mathbf{ff} \pi_{0} \parallel \text{if } tt \text{ then } tt \text{ else } \underline{a} \ (\lambda x^{\perp} x) \pi_{1}) \mapsto^{*} {}_{a}(\text{if } u \text{ then } tt \text{ else } \overline{a} \ \mathbf{ff} \pi_{0} \parallel tt) \mapsto tt$ and symmetrically  $O[tt/x][u/y] \mapsto^{*} tt$ . On the other hand,  $O[\mathbf{ff}/x][\mathbf{ff}/y] \mapsto^{*} \mathbf{ff}$ .

**Example 27** (Buyer and Vendor). We model the following transaction: a buyer tells a vendor a product name prod : String, the vendor computes the monetary cost price :  $\mathbb{N}$  of prod and communicates it to the buyer, the buyer sends back the credit card number card : String which is used to pay. We introduce the following functions:  $\operatorname{cost}$  :  $\operatorname{String} \to \mathbb{N}$  with input a product name prod and output its cost price;  $\operatorname{pay}_{-}\operatorname{for} : \mathbb{N} \to \operatorname{String}$  with input a price and output a credit card number card; use :  $\operatorname{String} \to \mathbb{N}$  that produces money using as input a credit card number card :  $\operatorname{String}$ . The buyer and the vendor are the contexts  $\mathcal{B}$  and  $\mathcal{V}$  of type Bool. The communication channel a is typed using the instance ( $\operatorname{String} \to \mathbb{N} \lor \operatorname{String}$ ) of the axiom  $(A \to B) \lor (B \to A)$ . The program is:

$$\begin{split} &{}_{a}(\mathcal{B}[\overline{a} \;(\mathsf{pay\_for}(\overline{a}\;(\mathsf{prod})))] \parallel \mathcal{V}[\mathsf{use}(\overline{a}\;(\mathsf{cost}(\underline{a}\;0)))]) \\ &\mapsto {}_{a}(\mathcal{B}[\overline{a}\;(\mathsf{pay\_for}(\underline{a}\;(\mathsf{prod})))] \parallel \mathcal{V}[\mathsf{use}(\overline{a}\;(\mathsf{cost}(\mathsf{prod})))]) \\ &\mapsto {}_{a}(\mathcal{B}[\overline{a}\;(\mathsf{pay\_for}(\underline{a}\;(\mathsf{prod})))] \parallel \mathcal{V}[\mathsf{use}(\overline{a}\;(\mathsf{price}))]) \\ &\mapsto {}_{a}(\mathcal{B}[\overline{a}\;(\mathsf{pay\_for}(\mathsf{price}))] \parallel \mathcal{V}[\mathsf{use}(\underline{a}\;(\mathsf{price}))]) \\ &\mapsto {}_{a}(\mathcal{B}[\overline{a}\;(\mathsf{card})] \parallel \mathcal{V}[\mathsf{use}(\underline{a}\;(\mathsf{price}))]) \mapsto {}_{a}(\mathcal{B}[\underline{a}\;(\mathsf{card})] \parallel \mathcal{V}[\mathsf{use}(\mathsf{card})]) \end{split}$$

Finally  $\mapsto \mathcal{V}[\mathsf{use}(\mathsf{card})]$ : the buyer has performed its duty and the vendor uses the card number to obtain the due payment.

**Example 28** (A Parallel Program for Computing  $\pi$ ). We implement in  $\lambda_{\parallel}$  a parallel program for computing arbitrary precise approximations of  $\pi$ . As is well known,  $\pi$  can be computed as follows:  $\pi = \lim_{l\to\infty} \frac{1}{l} \sum_{i=1}^{l} f(\frac{i-\frac{1}{2}}{l})$  where  $f(x) = \frac{4}{1+x^2}$ . For any given l, instead of calculating sequentially the whole sum  $\sum_{i=1}^{l} f(\frac{i-1/2}{l})$  and then dividing it by l, it is more efficient to distribute different parts of the sum to p parallel processes. When the processes terminate, they can send the results  $m_1, \ldots, m_p$ , as shown below, to a process which computes the final result  $\frac{1}{l}(m_1 + \ldots + m_p)$  (see also [21]). The graph in the following figure, in which we omit reflexive edges, is encoded by the axiom  $\mathcal{A} = (\mathbb{A}_1 \to \mathbb{A}_1 \land \bot) \lor \ldots \lor (\mathbb{A}_p \to \mathbb{A}_p \land \bot) \lor \mathbb{B} \to (\mathbb{B} \land \mathbb{A}_1 \land \ldots \land \mathbb{A}_p)$ .

To write the program, we add to  $\lambda_{\parallel}$  types and constants for rational numbers, together with function constants  $f_1, \ldots, f_p$ , sum such that

$$\mathbf{f}_k \, l \mapsto^* \sum_{i=(k-1)l/p+1}^{(kl/p)} f(\frac{i-\frac{1}{2}}{l}) \quad and \quad \mathsf{sum} \, \langle n_1, \dots, n_i \rangle \, l \mapsto^* \frac{1}{l} (n_1 + \dots + n_i)$$

thus computing respectively the p partial sums and the final result. The  $\lambda_{\parallel}$  term that takes as input the length l of the summation to be carried out and yields the corresponding approximation of  $\pi$  is:  $_{a}(\overline{a}(f_{1}l)\pi_{0} \parallel \ldots \parallel \overline{a}(f_{p}l)\pi_{0} \parallel \text{sum}(\underline{a}(\lambda x^{\perp} x)\pi_{1})l)$ . By instantiating  $\mathbb{A}_{1}, \ldots, \mathbb{A}_{p}$  in the extracted  $\mathcal{A}$  with the type Q of rational numbers, we type all displayed occurrences of a in the first p threads by  $\mathbb{Q} \to \mathbb{Q} \land \bot$ , and the last by  $\top \to (\top \land \mathbb{Q} \land \ldots \land \mathbb{Q})$ . Given any multiple n of p, we have

 $(a(\overline{a}(\mathbf{f}_1 \ l) \ \pi_0 \ \| \dots \| \ \overline{a}(\mathbf{f}_p \ l) \ \pi_0 \ \| \operatorname{sum} (\underline{a}(\lambda x^{\perp} \ x) \ \pi_1) \ l \ ))[n/l] = a(\overline{a}(\mathbf{f}_1 \ n) \ \pi_0 \ \| \dots \| \ \overline{a}(\mathbf{f}_p \ n) \ \pi_0 \ \| \operatorname{sum} (\underline{a}(\lambda x^{\perp} \ x) \ \pi_1) \ n \ ))[n/l] = a(\overline{a}(\mathbf{f}_1 \ n) \ \pi_0 \ \| \dots \| \ \overline{a}(\mathbf{f}_p \ n) \ \pi_0 \ \| \operatorname{sum} (\underline{a}(\lambda x^{\perp} \ x) \ \pi_1) \ n \ ))[n/l] = a(\overline{a}(\mathbf{f}_1 \ n) \ \pi_0 \ \| \dots \| \ \overline{a}(\mathbf{f}_p \ n) \ \pi_0 \ \| \operatorname{sum} (\underline{a}(\lambda x^{\perp} \ x) \ \pi_1) \ n \ ))[n/l] = a(\overline{a}(\mathbf{f}_1 \ n) \ \pi_0 \ \| \dots \| \ \overline{a}(\mathbf{f}_p \ n) \ \pi_0 \ \| \operatorname{sum} (\underline{a}(\lambda x^{\perp} \ x) \ \pi_1) \ n \ )$ 

$$\mapsto \ _{a}(\ \overline{a}(\sum_{i=1}^{(n/p)}f(\frac{i-\frac{1}{2}}{l}))\pi_{0}\parallel\ldots\parallel\overline{a}(\sum_{i=(p-1)n/p+1}^{n}f(\frac{i-\frac{1}{2}}{l}))\pi_{0}\parallel \text{sum}(\underline{a}\ (\lambda x^{\perp}\ x)\ \pi_{1})\ n\ ) \\ \mapsto^{*}\ \text{sum}(\langle\sum_{i=1}^{(n/p)}f(\frac{i-\frac{1}{2}}{l}))),\ldots,\sum_{i=(p-1)n/p+1}^{n}f(\frac{i-\frac{1}{2}}{l}))\rangle\ n\ \ \mapsto^{*}\ \frac{1}{n}\sum_{i=1}^{n}f(\frac{i-\frac{1}{2}}{n})$$

**Example 29** (A Parallel Floyd–Warshall Algorithm). We define a  $\lambda_{\parallel}$  term that implements a parallel version of the Floyd–Warshall algorithm. The algorithm takes as input a directed graph and outputs a matrix containing the length of the shortest path between each pair of nodes. Formally, the input graph is coded as a matrix I(0) and the nodes of the graphs are labeled as 1, 2, ..., n. Then the sequential Floyd–Warshall algorithm computes a sequence of matrixes I(1), ..., I(n), representing closer and closer approximations of the desired output matrix. In particular, the entry (i, j) of I(k) is the length of the shortest path connecting i and j such that every node of the path, except for the endpoints, is among the nodes 1, 2, ..., k. Now, each matrix I(k) can be easily computed from I(k-1). The idea is that passing through the node k might be better than passing only through the first k-1 nodes, or not. Hence in order to compute I(k+1) from I(k), one only needs to evaluate the right-hand side of the following equation:

$$I_{i,j}(k) = \min(I_{i,j}(k-1), I_{i,k}(k-1) + I_{k,j}(k-1))$$
(1)

Indeed,  $I_{i,j}(k-1)$  represents the shortest path between i, j passing only through the first k-1 nodes, while  $I_{i,k}(k-1) + I_{k,j}(k-1)$  computes the shortest path between i, j that passes through

k and the first k - 1 nodes. To speed-up this computation, we create a parallel process for each row I(k) of the matrix and put the process in charge of computing the row. We say that the *i*-th process has to compute the *i*-th row  $I_i(k)$ . Which information does it need? Actually, only two rows:  $I_i(k-1)$  and  $I_k(k-1)$ . Hence at each round k the process-*i* only lacks the row  $I_k(k-1)$ to perform its computation. This row can be communicated to process-*i* by the process-*k*, in charge to compute that row. These considerations lead to a well-known parallel algorithm.

#### The ring Floyd–Warshall algorithm

1. Take the input  $n \times n$ -matrix I(0) and distribute the *i*-th row  $I_i(0)$  to process-*i*. Organize the *n* processes in a ring structure such as the following (see [21]), omitting reflexive edges:

$$(*) I_{7}() I_{2}() I_{2}() I_{3}() I_{3}() I_{4}() I_$$

2. For k = 1 to n, starting from process-k, let all the processes forward the row  $I_k(k-1)$  to their successors in the ring, until the process k+1 receives again the same row. After the row has circulated, let the processes compute in parallel the rows of the matrix I(k). 3. Let I(n) be the output.

In this algorithm, at any stage k, communicating the required row  $I_k(k-1)$  through the ring structure requires less time compared to computing each row of the matrix, therefore the overhead of the communication is compensated by the speed-up in the matrix computation.

#### A $\lambda_{\parallel}$ program for the Floyd–Warshall algorithm

To write a  $\lambda_{\parallel}$  program that computes the ring Floyd–Warshall Algorithm, we add integers to  $\lambda_{\parallel}$ , as usual, and we denote with A the function type corresponding to the rows of the matrixes I(k) computed by the algorithm. The expression  $I_x(y)$  represents the function computing the  $x^{th}$  line of the matrix at the  $y^{th}$  stage of the algorithm. We assume that the value of  $I_x(y)$  contains information about x and y. We also add the constant function  $f : A \wedge A \rightarrow A$  such that:  $f\langle I_i(k-1), I_k(k-1) \rangle = I_i(k)$  and  $f\langle I_z(l), I_m(n) \rangle = I_z(l)$ .

The first equation implements the calculations needed for equation (1). The second comes into play at the end of each iteration of the algorithm, when the process that receives twice the same row discards it, starts sending its own row, and begins the next iteration of the algorithm.

As handy notation, for any three terms u, v, s we define  $(u, v)^{1s}$  as u(vs), and  $(u, v)^{n+1s}$  for n > 0 as  $u(v((u, v)^{n}s))$ . Moreover, for any two terms u, s we define  $(u, \pi_{i})^{1s}$  as  $u(s\pi_{i})$ , and  $(u, \pi_{i})^{n+1s}$  for n > 0 as  $u(((u, \pi_{i})^{n}s)\pi_{i})$ . Intuitively,  $(u, v)^{n}s$  represents what we obtain if we take a term s and then apply alternately v and u to the term resulting from the last operation. We obtain, ultimately, a term of the form  $u(v(u(v(\ldots u(vs)\ldots))))$  in which and u and v occur n times each. The notation  $(u, \pi_{i})^{n}s$  is analogous, but instead of applying v we apply the projection  $\pi_{i}$ . The n processes that run in parallel during the execution of the algorithm are (1 < i < n): Process  $p_{1}: (f, \underline{a})^{n} I_{1}(0) \parallel ((\overline{a}, \pi_{1})^{n} \underline{a} I_{1}(0))\pi_{0} \parallel (\overline{a} I_{1}(0))\pi_{0}$ 

Process  $p_i: (f,\underline{a})^{n+1} I_i(0) \parallel ((\overline{a},\pi_1)^{n+1-i}(\underline{a},\pi_1)^i \underline{a} I_i(0)) \pi_0 \parallel (\overline{a} (f,\underline{a})^i I_i(0)) \pi_0 \parallel ((\overline{a},\pi_1)^{i-1} \underline{a} I_i(0)) \pi_0$ Process  $p_n: (f,\underline{a})^{n+1} I_n(0) \parallel (\overline{a} ((f,\underline{a})^n I_n(0))) \pi_0 \parallel ((\overline{a},\pi_1)^{n-1} \underline{a} I_n(0)) \pi_0$ 

For an intuitive reading of the parts of these terms, consider the notation  $(f, \underline{a})^m t$ . This notation represents a term of the form  $f(\underline{a}(\ldots f(\underline{a}t)\ldots))$ . Only one operation can be immediately performed with a term like this: using the innermost application of a and consume at to receive a message. The received message will be the argument of the innermost f. The value that fcomputes, in turn, will be the argument of the next communication channel. Terms of this form alternate two phases: one in which they receive, and one in which they apply f to the received message. The terms  $(\overline{a}, \pi_1)^m t$  have the form  $\overline{a}((\ldots \overline{a}(t\pi_1)\ldots)\pi_1)$ . These terms project, send and receive; and then start over. The projections are used to select messages from the tuple of received messages. The selected messages are not used, but forwarded to another process.

Each process  $p_i$  (i > 1) is a parallel composition of four threads: the first and the third compute the row  $I_i(k)$ , and the second and the fourth send and forward rows. The process  $p_1$ behaves in the same way, but has three threads: the first computes the rows  $I_1(k)$ , the second receives and forwards rows, and the third sends its own row. The term implementing the algorithm is  $_a(p_1 \parallel \ldots \parallel p_n)$ . The axiom extracted from the ring structure above by Proc. 1 is  $\mathcal{A} = (\mathbb{A}_1 \to \mathbb{A}_1 \land \mathbb{A}_n) \lor (\mathbb{A}_2 \to \mathbb{A}_2 \land \mathbb{A}_1) \lor \ldots \lor (\mathbb{A}_{n-1} \to \mathbb{A}_{n-1} \land \mathbb{A}_{n-2}) \lor (\mathbb{A}_n \to \mathbb{A}_n \land \mathbb{A}_{n-1})$ . We instantiate  $\mathbb{A}_1, \ldots, \mathbb{A}_n$  in  $\mathcal{A}$  with  $\mathcal{A}$  and type a by  $\mathcal{A} \to \mathcal{A} \land \mathcal{A}$ .

We instantiate  $\mathbb{A}_1, \ldots, \mathbb{A}_n$  in  $\mathcal{A}$  with  $\mathcal{A}$  and type a by  $\mathcal{A} \to \mathcal{A} \wedge \mathcal{A}$ . We present some steps of the execution of the instance  $_a(p_1 \parallel p_2 \parallel p_3)$ . See the Appendix for the unabridged version of this reduction. According to our normalization principles, we normalize the threads sending messages right before they communicate; then, we normalize the rightmost threads receiving the messages. All other  $\mathbf{L}$  reductions are performed in-between communications. Finally, we make sure to contract all intuitionistic redexes before the beginning of a new iteration of the algorithm. We start with:

- $\mapsto^{*} a \Big( \Big( f(\underline{a} (f(\underline{a} I_{1}(0))))) \| (\overline{a} ((\overline{a} ((\overline{a} ((\underline{a} I_{1}(0))\pi_{1}))\pi_{1}))\pi_{0}) \| (\overline{a} I_{1}(0))\pi_{0} \Big) \\ \| \Big( f(\underline{a} (f(\underline{a} (f(\underline{a} I_{2}(0))))))) \| (\overline{a} ((\overline{a} ((\underline{a} ((\underline{a} I_{2}(0))\pi_{1}))\pi_{1}))\pi_{1}))\pi_{0}) \\ \| (\overline{a} (f(\underline{a} (f(\underline{a} I_{2}(0)))))\pi_{0}) \| (\overline{a} ((\underline{a} I_{2}(0))\pi_{1}))\pi_{0} \Big) \Big)$ 
  - $\|\left(f(\underline{a}\left(f(\underline{a}\left(f(\underline{a}\left(f(\underline{a}\left(f(\underline{a}\left(I_{3}(0)\right))\right)\right)\right))\|\left(\overline{a}\left(f(\underline{a}\left(f(\underline{a}\left(f(\underline{a}\left(I_{3}(0)\right)\right)\right)\right)\right))\pi_{0}\|\left(\overline{a}\left((\overline{a}\left((\underline{a}\left(I_{3}(0)\right)\pi_{1}\right))\pi_{1}\right))\pi_{0}\right)\right)$

We first transmit the value  $I_1(0)$  from the first process to the second process and we move the focus to the next term. As an aid to the reader, we display between \*\* the occurrences of the message that are involved in the reduction:

 $a\left(\left(f(\underline{a}\left(f(\underline{a}\left(f(\underline{a}I_{1}(0))\right)\right)\right) \parallel (\overline{a}\left((\overline{a}\left((\underline{a}I_{1}(0))\pi_{1}\right))\pi_{1}\right))\pi_{0}\parallel (\star \underline{a}I_{1}(0)^{\star})\pi_{0}\right)\right)$ 

 $\| \left( \overline{a} \left( f(\underline{a} \left( f\langle I_2(0), {}^{\star} I_1(0)^{\star} \rangle \right) \right) \right) \pi_0 \| \left( \overline{a} \left( \langle I_2(0), {}^{\star} I_1(0)^{\star} \rangle \pi_1 \right) \right) \pi_0 \right)$ 

 $\parallel \left( f(\underline{a} \left( f(\underline{a} \left( f(\underline{a} \left( f(\underline{a} \left( I_{3}(0) \right) \right) \right) \right))) (\overline{a} \left( f(\underline{a} \left( f(\underline{a} \left( f(\underline{a} \left( I_{3}(0) \right) \right) \right)))) \pi_{0} \parallel (\overline{a} \left( (\overline{a} \left( (\underline{a} \left( \langle 0, I_{3}(0) \rangle \pi_{1} \right) \right) \pi_{1} \right)) \pi_{0} \right) \right) (\overline{a} \left( f(\underline{a} f(\underline$ 

**Remark 30.**  $I_2(0)$  is not destroyed by the communication but saved using the memorization mechanism. This term is needed, indeed, by the function f in order to compute  $I_2(1)$ .

We normalize the receiver of the previous communication and keep normalizing the other redexes in parallel, thus the rightmost thread of the second process become ready to forward the value  $I_1(0)$  to the third thread:

 $\mapsto^*{}_a\left(\left(f(\underline{a}\left(f(\underline{a}\left(f(\underline{a}\left(f(\underline{a}\left(I_1(0)\right)\right)\right)\right) \| (\overline{a}\left((\overline{a}\left((\overline{a}\left((\underline{a}\left(I_1(0)\right)\pi_1\right)\right)\pi_1\right))\pi_1\right))\pi_0 \| (\underline{a}\left(I_1(0)\right)\right))\pi_0\right)$ 

 $\| \left( f(\underline{a} \left( \underline{a} \left( \underline{a} \right) \right) \pi_1 \right) \right) \pi_1 \right) \right) \pi_1 \right) \right) \pi_1 \right) \right) \|$ 

 $\| (\overline{a} (f(\underline{a} (f\langle I_2(0), I_1(0) \rangle)))) \pi_0 \| ( *\overline{a} I_1(0) * ) \pi_0 )$ 

 $\left\| \left( f(\underline{a} \left( f(\underline{a} \left( f(\underline{a} \left( f(\underline{a} \left( f(\underline{a} \left( I_3(0) \right) \right) \right) \right) \right) \| \left( \overline{a} \left( f(\underline{a} f(\underline{a} \left( f(\underline{a} f(\underline{a} \left( f(\underline{a} f(\underline{a} \left( f(\underline{a} f$ 

The third thread receives  $I_1(0)$  and the computation continues:  $\mapsto \dots$ 

At the end of the first of the three cycles, the second process receives  $I_1(0)$  again:

 $\mapsto^* a \left( \left( f(\underline{a} \left( f(\underline{a} \left( f(\underline{a} \left( f(\underline{a} \left( f(\underline{a} \left( I(0), I_1(0) \right) \right) \right) \right) \| (\overline{a} \left( (\overline{a} \left( ( \left( \underline{a} \left( I(0)^* \right) \pi_1 \right) \right) \pi_1 \right) \pi_0 \| \langle I_1(0), I_1(0) \rangle \pi_0 \right) \right) \right)$ 

 $\| \left( f(\underline{a} \left( f(\underline{a} \left( f\langle I_2(0), \star I_1(0)^{\star} \right\rangle \right) \right) \right)) \| (\overline{a} \left( (\overline{a} \left( (\underline{a} \left( \langle I_1(0), \star I_1(0)^{\star} \right\rangle \pi_1) \pi_1 \right) \right) \pi_1 \right) ) \pi_1 \right)$ 

 $\| (\overline{a} (f \langle I_2(0), {}^{\star}I_1(0)^{\star} \rangle))) \pi_0 \| \langle I_1(0), {}^{\star}I_1(0)^{\star} \rangle \pi_0 )$ 

 $\| \left( f(\underline{a} f$ 

The reception of  $I_1(0)$  exhausts all forwarding channels of the second process in the rightmost thread and triggers the communication of the  $I_2(1)$  just computed, thus starting the second cycle:

 $\mapsto^{*} a \left( \left( f(\underline{a} (I_{1}(1))) \right) \| (\overline{a} ((\overline{a} ((\underline{a} I_{1}(0))\pi_{1}))\pi_{1}))\pi_{0} \| (\langle I_{1}(0), I_{1}(0) \rangle)\pi_{0} \right) \\ \| (f(\underline{a} (f(\underline{a} I_{2}(1)))) \| (\overline{a} ((\overline{a} ((\underline{a} I_{1}(0))\pi_{1}))\pi_{1}))\pi_{0} \| ({}^{*}\overline{a} I_{2}(1)^{*} )\pi_{0} \| \langle I_{1}(0), I_{1}(0) \rangle\pi_{0} ) \\ \| (f(\underline{a} (f(\underline{a} (f(\underline{a} I_{3}(1)))))) \| (\overline{a} (f(\underline{a} (f(\underline{a} I_{3}(1)))))\pi_{0} \| (\overline{a} ((\underline{a} I_{1}(0))\pi_{1}))\pi_{0} ) ) \\ \mapsto \dots$ 

The reduction continues in the same fashion for two other cycles, until the third process does not contain any occurrence of a anymore:

 $\mapsto^{*}{}_{a} \left( \left( f \langle I_{1}(2), I_{3}(2) \rangle \parallel (\overline{a} I_{3}(2)) \pi_{0} \parallel \langle I_{1}(0), I_{1}(0) \rangle \pi_{0} \right) \\ \parallel \left( f \langle I_{2}(2), I_{3}(2) \rangle \parallel (\underline{a} I_{3}(2)) \pi_{0} \parallel \langle I_{2}(1), I_{2}(1) \rangle \pi_{0} \parallel \langle I_{1}(0), I_{1}(0) \rangle \pi_{0} \right) \\ \parallel \left( f \langle I_{3}(2), I_{3}(2) \rangle \parallel \langle I_{3}(2), I_{3}(2) \rangle \pi_{0} \parallel \langle I_{2}(1), I_{2}(1) \rangle \pi_{0} \right) \right)$ 

We then compute all occurrences of f in parallel:

 $\mapsto^*{}_a((I_1(3) \parallel (\overline{a} I_3(2))\pi_0 \parallel \langle I_1(0), I_1(0) \rangle \pi_0) \\ \parallel (I_2(3) \parallel (\underline{a} I_3(2))\pi_0 \parallel \langle I_2(1), I_2(1) \rangle \pi_0 \parallel \langle I_1(0), I_1(0) \rangle \pi_0) \\ \parallel (I_3(3) \parallel \langle I_3(2), I_3(2) \rangle \pi_0 \parallel \langle I_2(1), I_2(1) \rangle \pi_0))$ 

We use a simplification reduction to remove the intermediate results no more needed and only keep the threads containing the result of the computation:  $\mapsto^* I_1(3) \parallel I_2(3) \parallel I_3(3)$ .

#### **Conclusions and Related Work**

We introduced  $\lambda_{\parallel}$ , a simple and yet expressive typed parallel  $\lambda$ -calculus based on (suitable fragments of) propositional intermediate logics.  $\lambda_{\parallel}$  is based on and greatly simplifies the calculi in [4, 3, 2]. The basic communication reductions in the calculi  $\lambda_{CL}$  [3] and  $\lambda_G$  [2] implement particular  $\lambda_{\parallel}$  communications: the message passing mechanisms based on classical logic and on Gödel logic, respectively.  $\lambda_{CL}$  and  $\lambda_G$  also contain additional reductions (*permutations* and full cross), needed for proving weak normalization and the subformula property. These calculi were generalized in [4], that introduces a Curry-Howard correspondence for all propositional intermediate logics characterized by classical disjunctive tautologies, the axioms for classical and Gödel logic being particular cases. The reductions in [4] are the same as for  $\lambda_{CL}$  and  $\lambda_G$ but their activation procedure is based on transmitting values and thus it is logic independent. The simple(r)  $\lambda_{\parallel}$  reduction allows us to encode interesting parallel programs in Eden – an extension of Haskell [21, 22], to prove strong normalization and to specify process networks in an automated way. The price to pay is the lack of the subformula property and the fact that well-typed  $\lambda_{\parallel}$ -terms might contain deadlock. In a model of parallel computation the latter is an unwanted feature, especially in absence<sup>3</sup> of recursion or fixed points. Unlike  $\lambda_{\parallel}$ , the typed versions of the  $\pi$ -calculus, starting with the seminal work in [9], are usually deadlock free, e.g., [29, 28, 10].

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<sup>&</sup>lt;sup>3</sup>Note that as the number of  $\lambda_{\parallel}$  communications is not type-determined, the system is compatible with the addition of a recursion operator.

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# A Appendix

We present here the unabridged version of the reduction in Example 29. **Full reduction of the ring Floyd–Warshall algorithm in**  $\lambda_{\parallel}$ . In this reduction, we omit explanations but we indicate the selected process that can receive messages by underlining it.



 $\|\langle I_1(0), I_1(0)\rangle \pi_0\rangle$  $\| (a(f(a(f\langle I_2(0), I_1(0) \rangle))))\pi_0 \| (aI_1(0))\pi_0) \|$  $\| (a(f(a(f(a(f(a(f(I_3(0), I_1(0)))))))\pi_0 \| (a((aI_1(0))\pi_1))\pi_0)))$  $\mapsto^*{}_a((f(a(f(aI_1(1)))) \parallel (a((a((aI_1(0))\pi_1))\pi_1))\pi_0 \parallel \langle I_1(0), I_1(0) \rangle \pi_0))$  $\| (a(f(aI_2(1))) \| aI_1(0))\pi_0)$  $\| (f(a(f(a(f(aI_3(1)))))) \| (a((a((a(I_1(0)))\pi_1))\pi_1))\pi_0)$  $\| (a(f(a(f(aI_3(1))))))\pi_0 \| (a((aI_1(0))\pi_1))\pi_0))$  $\mapsto^*{}_a((f(a(f(aI_1(1)))) \parallel (a((a((aI_1(0))\pi_1))\pi_1))\pi_0 \parallel \langle I_1(0), I_1(0) \rangle \pi_0))$  $\| (a(f\langle I_2(1), I_1(0) \rangle)) \pi_0 \| \langle I_1(0), I_1(0) \rangle \pi_0 \rangle$  $\| (f(a(f(a(f(aI_3(1)))))) \| (a((a((a(I_1(0)))\pi_1))\pi_1))\pi_0)$  $\| (a(f(a(f(aI_3(1))))))\pi_0 \| (a((aI_1(0))\pi_1))\pi_0))$  $\mapsto^*{}_a((f(a(f(aI_1(1)))) \parallel (a((a((aI_1(0))\pi_1))\pi_1))\pi_0 \parallel (\langle I_1(0), I_1(0) \rangle)\pi_0))$  $\| \left( f(a(f(aI_2(1)))) \| (a((a((aI_1(0))\pi_1))\pi_1))\pi_0 \| (a(I_2(1)))\pi_0 \| (\langle I_1(0), I_1(0) \rangle)\pi_0 \right)$  $\| (a(f(a(f(aI_3(1))))))\pi_0 \| (a((aI_1(0))\pi_1))\pi_0))$  $\mapsto^*{}_a((f(a(f(aI_1(1)))) \parallel (a((a((aI_1(0))\pi_1))\pi_1))\pi_0 \parallel \langle I_1(0), I_1(0) \rangle \pi_0))$  $\| \left( f(a(f(aI_2(1)))) \| (a((a((aI_1(0))\pi_1))\pi_1))\pi_0 \| (aI_2(1))\pi_0 \| \langle I_1(0), I_1(0) \rangle \pi_0 \right)$  $\| (f(a(f(a(f\langle I_3(1), I_2(1)\rangle)))) \| (a((a(\langle I_3(1), I_2(1)\rangle\pi_1)\pi_1))\pi_0) \|$  $\| (a(f(a(f\langle I_3(1), I_2(1)\rangle))))\pi_0 \| (a(\langle I_1(0), I_2(1)\rangle\pi_1))\pi_0))$  $\mapsto^*{}_a((f(a(f(aI_1(1)))) \parallel (a((a((aI_1(0))\pi_1))\pi_1))\pi_0 \parallel \langle I_1(0), I_1(0) \rangle \pi_0))$  $\| \left( f(a(f(aI_2(1)))) \| (a((a((aI_1(0))\pi_1))\pi_1))\pi_0 \| (aI_2(1))\pi_0 \| \langle I_1(0), I_1(0) \rangle \pi_0 \right)$  $\| (f(a(f(a(f\langle I_3(1), I_2(1)\rangle)))) \| (a((aI_2(1))\pi_1))\pi_0) \|$  $\| (a(f(aI(f\langle I_3(1), I_2(1)\rangle))))\pi_0 \| (aI_2(1))\pi_0) )$  $\mapsto^*{}_a((f(a(f\langle I_1(1), I_2(1)\rangle)) \parallel (a((a(\langle I_1(0), I_2(1)\rangle\pi_1))\pi_1))\pi_0 \parallel \langle I_1(0), I_1(0)\rangle\pi_0)$  $\| \left( f(a(f(aI_2(1)))) \| (a((a((aI_1(0))\pi_1))\pi_1))\pi_0 \| (aI_2(1))\pi_0 \| \langle I_1(0), I_1(0) \rangle \pi_0 \right)$  $\| (f(a(f(a(f(I_3(1), I_2(1)))))) \| (a((aI_2(1))\pi_1))\pi_0)$  $\| (a(f(a(f\langle I_3(1), I_2(1)\rangle))))\pi_0 \| (aI_2(1))\pi_0))$  $\mapsto^*{}_a((f(a(f\langle I_1(1), I_2(1)\rangle)) \parallel (a((aI_2(1))\pi_1))\pi_0 \parallel \langle I_1(0), I_1(0)\rangle\pi_0)$  $\| \left( f(a(f(aI_2(1)))) \| (a((a((aI_1(0))\pi_1))\pi_1))\pi_0 \| (aI_2(1))\pi_0 \| \langle I_1(0), I_1(0) \rangle \pi_0 \right)$  $\| (f(a(f(a(f\langle I_3(1), I_2(1)\rangle)))) \| (a((aI_2(1))\pi_1))\pi_0$  $\| (a(f(a(f\langle I_3(1), I_2(1)\rangle))))\pi_0 \| (aI_2(1))\pi_0))$ 

 $\mapsto^*{}_a((f(a(f\langle I_1(1), I_2(1)\rangle)) \parallel (a((aI_2(1))\pi_1))\pi_0 \parallel \langle I_1(0), I_1(0)\rangle\pi_0))$  $\| (f(a(f\langle I_2(1), I_2(1)\rangle)) \| (a((a(\langle I_1(0), I_2(1)\rangle\pi_1))\pi_1))\pi_0$  $\| \langle I_2(1), I_2(1) \rangle \pi_0 \| \langle I_1(0), I_1(0) \rangle \pi_0 \rangle$  $\| (f(a(f(a(f\langle I_3(1), I_2(1)\rangle)))) \| (a((aI_2(1))\pi_1))\pi_0$  $\| (a(f(a(f\langle I_3(1), I_2(1)\rangle))))\pi_0 \| (aI_2(1))\pi_0))$  $\mapsto^*{}_a((f(aI_1(2)) \parallel (a((aI_2(1))\pi_1))\pi_0 \parallel \langle I_1(0), I_1(0) \rangle \pi_0))$  $\| (f(aI_2(2)) \| (a((aI_2(1))\pi_1))\pi_0 \| \langle I_2(1), I_2(1) \rangle \pi_0 \| \langle I_1(0), I_1(0) \rangle \pi_0)$  $\| \left( f(a(f(aI_3(2)))) \| (a((aI_2(1))\pi_1))\pi_0 \| (a(f(aI_3(2))))\pi_0 \| (aI_2(1))\pi_0 \right) \right)$  $\mapsto^*{}_a((f(aI_1(2)) \parallel (a((aI_2(1))\pi_1))\pi_0 \parallel \langle I_1(0), I_1(0) \rangle \pi_0))$  $\| (f(aI_2(2)) \| (a((aI_2(1))\pi_1))\pi_0 \| \langle I_2(1), I_2(1) \rangle \pi_0 \| \langle I_1(0), I_1(0) \rangle \pi_0)$  $\| (f(a(f\langle I_3(2), I_2(1)\rangle)) \| (a(\langle I_2(1), I_2(1)\rangle\pi_1))\pi_0$  $\| (a(f\langle I_3(2), I_2(1) \rangle)) \pi_0 \| \langle I_2(1), I_2(1) \rangle \pi_0) \|$  $\mapsto^* a((f(aI_1(2)) \parallel (a((aI_2(1))\pi_1))\pi_0 \parallel \langle I_1(0), I_1(0) \rangle \pi_0))$  $\| (f(aI_2(2)) \| (a((aI_2(1))\pi_1))\pi_0 \| \langle I_2(1), I_2(1) \rangle \pi_0 \| \langle I_1(0), I_1(0) \rangle \pi_0)$  $\| (f(aI_3(2)) \| (aI_2(1))\pi_0 \| (aI_3(2))\pi_0 \| \langle I_2(1), I_2(1) \rangle \pi_0))$  $\mapsto^*{}_a((f\langle I_1(2), I_3(2)\rangle \parallel (a(\langle I_2(1), I_3(2)\rangle \pi_1))\pi_0 \parallel \langle I_1(0), I_1(0)\rangle \pi_0))$  $\| (f(aI_2(2)) \| (a((aI_2(1))\pi_1))\pi_0 \| \langle I_2(1), I_2(1) \rangle \pi_0 \| \langle I_1(0), I_1(0) \rangle \pi_0)$  $\| (f(aI_3(2)) \| (aI_2(1))\pi_0 \| (aI_3(2))\pi_0 \| \langle I_2(1), I_2(1) \rangle \pi_0) )$  $\mapsto^*{}_a((f\langle I_1(2), I_3(2)\rangle \parallel (aI_3(2))\pi_0 \parallel \langle I_1(0), I_1(0)\rangle \pi_0))$  $\| (f(aI_2(2)) \| (a((aI_2(1))\pi_1))\pi_0 \| \langle I_2(1), I_2(1) \rangle \pi_0 \| \langle I_1(0), I_1(0) \rangle \pi_0)$  $\| (f(aI_3(2)) \| (aI_2(1))\pi_0 \| (aI_3(2))\pi_0 \| \langle I_2(1), I_2(1) \rangle \pi_0))$  $\mapsto^*{}_a((f\langle I_1(2), I_3(2)\rangle \parallel (aI_3(2))\pi_0 \parallel \langle I_1(0), I_1(0)\rangle \pi_0))$  $\| (f \langle I_2(2), I_3(2) \rangle \| (a(\langle I_2(1), I_3(2) \rangle \pi_1)) \pi_0 \| \langle I_2(1), I_2(1) \rangle \pi_0 \| \langle I_1(0), I_1(0) \rangle \pi_0)$  $\| (f(aI_3(2)) \| (aI_2(1))\pi_0 \| (aI_3(2))\pi_0 \| \langle I_2(1), I_2(1) \rangle \pi_0))$  $\mapsto^*{}_a((f\langle I_1(2), I_3(2)\rangle \parallel (aI_3(2))\pi_0 \parallel \langle I_1(0), I_1(0)\rangle \pi_0))$  $\| (f \langle I_2(2), I_3(2) \rangle \| (aI_3(2)) \pi_0 \| \langle I_2(1), I_2(1) \rangle \pi_0 \| \langle I_1(0), I_1(0) \rangle \pi_0)$  $\| (f(aI_3(2)) \| (aI_2(1))\pi_0 \| (aI_3(2))\pi_0 \| \langle I_2(1), I_2(1) \rangle \pi_0))$  $\mapsto^*{}_a((f\langle I_1(2), I_3(2)\rangle \parallel (aI_3(2))\pi_0 \parallel \langle I_1(0), I_1(0)\rangle \pi_0)$  $\| (f \langle I_2(2), I_3(2) \rangle \| (aI_3(2))\pi_0 \| \langle I_2(1), I_2(1) \rangle \pi_0 \| \langle I_1(0), I_1(0) \rangle \pi_0)$  $\| \left( f \langle I_3(2), I_3(2) \rangle \| \langle I_2(1), I_3(2) \rangle \pi_0 \| \langle I_3(2), I_3(2) \rangle \pi_0 \| \langle I_2(1), I_2(1) \rangle \pi_0 \right) \right)$  $\mapsto^*{}_a((I_1(3) \parallel (aI_3(2))\pi_0 \parallel \langle I_1(0), I_1(0) \rangle \pi_0))$  $\| (I_2(3) \| (aI_3(2))\pi_0 \| \langle I_2(1), I_2(1) \rangle \pi_0 \| \langle I_1(0), I_1(0) \rangle \pi_0)$  $\| (I_3(3) \| \langle I_2(1), I_3(2) \rangle \pi_0 \| \langle I_3(2), I_3(2) \rangle \pi_0 \| \langle I_2(1), I_2(1) \rangle \pi_0) )$ 

 $\mapsto^* I_1(3) \parallel I_2(3) \parallel I_3(3)$